IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of

WASHIO, H. et al. Atty. Ref.: 1035-484; Confirmation No. 9130

Appl. No. 10/733,395 TC/A.U. 2629

Filed: December 12, 2003 Examiner: Sherman, Stephen G.

For: DISPLAY DEVICE

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Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

RULE 132 DECLARATION OF KAZUHIRO MAEDA

Pursuant to Rule 132, I hereby declare as follows:

1. I, Kazuhiro Maeda, am employed by Sharp Kabushiki Kaisha, headquartered in Osaka, Japan, assignee of the above-listed patent application. I am also listed as an inventor on the instant patent application.

2. My current job at Sharp involves design of drive circuitry for LCDs. I have been working in the field of displays for at least about 10 years and am one of at least ordinary skill in the art of displays; I began working for Sharp in 1997.

3. The description set forth in the instant specification at page 5, lines 10-21 (and Figs. 11-12), relied on by the Examiner in the rejection, is *not* "prior art." Thus, it will be

appreciated that the problem recited from page 5, line 22 to page 7, line 7, of the instant specification is not prior art to the instant application.

4. There was a <u>translation error</u> in preparing the U.S. application 10/733,395. The original text in the Japanese priority application includes a word which should have been accurately translated as "modification" – but this was erroneously translated as "typical." This word "typical" (at page 5, line 10 of the specification) is an incorrect translation. The Examiner's rejection relies on this incorrectly translated word. Thus, the discussion at page 5, lines 10-13, of the instant specification is not prior art.

5. As described at page 5, lines 4-7 of the specification, the applicant had proposed a "structure of providing a plurality of driving circuits, which are individually driven or driven together, to at least one of the data signal line driving circuit or the scanning signal line driving circuit" (referred to as Arrangement A for convenience). Applicant had considered making modifications to Arrangement A, and, as an example of such a modification, thought of making the arrangement in which "one of the plurality of driving circuits are supplied with two-systems clock signal, and the remaining driving circuits are supplied with one system clock signal" (page 5, lines 10-13). This is not prior art to the instant application. This was incorrectly referred to as a "typical arrangement" and was illustrated in Fig. 11, even though the intended meaning was "a representative example of possible modifications considered by the Applicant." The passage at page 5, lines 10-13, of the specification should have been translated as follows: "As a possible modification, this structure could be arranged so that one of the plurality of driving

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circuits are supplied with two-systems clock signal, and the remaining driving circuits are supplied with one system clock signal."

6. Thus, Figs. 11-12, and the original specification at page 5, lines 10-21, are not prior art. Moreover, the "problems" at page 5, line 22, to page 7, line 7, of the original specification are not prior art problems.

7. The language "the described display device" at page 7, lines 17-18, beginning with "In recent years . . ." refers to the "typical display device" at page 2, line 4, of the instant specification (not what is discussed at page 5, lines 10-21 or Fig. 11).

8. I hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code and that such willful false statements may jeopardize the validity of the application or any patent issued thereon.

Respectfully submitted,

By: _Kazuhiro Maeda_
Kazuhiro Maeda